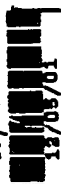


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35474 U.S. PTO
09/683243



	Subclass
	Class
ISSUE CLASSIFICATION	

PATENT NUMBER

Best Available Copy
U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>LM3</u> U.A. <u>W</u>	PATENT DATE
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APPLICATION NO. 09/683243	CONT/PRIOR D	CLASS 326 <u>713</u>	SUBCLASS <u>300</u>	ART UNIT 2819	EXAMINER <u>Lee</u> <u>Wang</u>
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APPLICANTS

Chia-Hsing Yu
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2115

TITLE

Integrated level shift and override circuit for a processor system

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL CLASSIFICATION									
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____					
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				Amount Due	Date Paid
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